Appl.No.: 09/899,744
Amendment dated February 27, 2004
Response to Office Action mailed November 28, 2003

REMARKS/ARGUMENTS

Claims 1-6 are pending in the application. Reexamination and reconsideration are hereby requested.

Claim 1 was rejected as anticipated by Slanina. The Examiner pointed to Slanina Figure 2.

Applicants reply that Slanina does not suggest transmitting only part of an encoded packet; namely, the rate ½ codes of Slanina would be packets with convolutions using both g1 and g2, not the either g1 or g2 of claim 1.

Claims 2-5 were rejected as unpatentable over Slanina in view of Kallel. The Examiner repeated the foregoing cite of Slanina Figure 2.
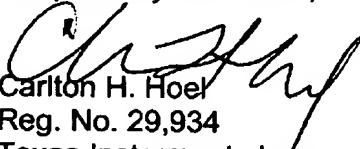
Applicants repeat the argument that Slanina does not suggest separate transmissions of the convolutions making up a rate ½ (or other rate less than 1) code.

Claim 6 was rejected as unpatentable over Slanina in view of Kallel and Chase. The Examiner repeated the foregoing cite of Slanina Figure 2.

Applicants again repeat the argument that Slanina does not suggest separate transmissions of the convolutions making up a rate ½ (or other rate less than 1)

The Appendix contains a proposed corrected Figure 1 which includes the step numbers from the application pages 3-5.

Respectfully submitted,

Carlton H. Hoel
Reg. No. 29,934
Texas Instruments Incorporated
PO Box 655474, M/S 3999
Dallas, Texas 75265
972.917.4365

Appl.No.: 09/899,744
Amendment dated February 27, 2004
Response to Office Action mailed November 28, 2003

Appendix   proposed corrected Figure 1

Transmitter

Receiver

(1) block of data bits
with CRC appended

(2) rate 1/2 encoding
   -g_1 convolution
   -g_2 convolution

(3) transmit g1-coded packet

(4) decode; CRC correct?

yes,
pass data bits

(5) transmit g2-coded packet

no,
ARQ

decode; CRC correct?

(6a) yes,
pass data bits

no

(6b) combine received packets
Viterbi decode, CRC correct?

(7a) yes,
pass data bits

(7b) no, discard earliest
packet, ARQ

(re)transmit g1-coded packet

decode; CRC correct?

(8a) yes,
pass data bits

(9b) no,
discard earliest packet,
ARQ

no

(8b) combine received packets
Viterbi decode, CRC correct?

(9a) yes,
pass data bits

Page 6